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<b>TRANSMITTAL FORM</b>  (to be used for all correspondence after initial filing)		Application Number	10/786994-Conf. #4477
		Filing Date	February 25, 2004
		First Named Inventor	Richard Schubert
		Art Unit	2188
		Examiner Name	Song, Jasmine
Total Number of Pages in This Submission	3	Attorney Docket Number	A0312.70523US00

ENCLOSURES (Check all that apply)		
<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance Communication to TC
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm Name	WOLF, GREENFIELD & SACKS, P.C.	
Signature	<i>William R. McClellan</i>	
Printed name	William R. McClellan	
Date	June 28, 2006	Reg. No. 29,409

Certificate of Mailing Under 37 CFR 1.8(a)	
I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as First Class Mail, in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.	
Dated: <u>June 28, 2006</u>	Signature: <u>Doris A. Champagne</u> (Doris A. Champagne)



Docket No.: A0312.70523US00  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Richard Schubert et al.  
Serial No.: 10/786994  
Confirmation No.: 4477  
Filed: February 25, 2004  
For: HIGH/LOW PRIORITY MEMORY  
Examiner: Song, Jasmine  
Art Unit: 2188

**Certificate of Mailing Under 37 CFR 1.8(a)**

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Dated: June 28, 2006

Doris A. Champagne  
Doris A. Champagne

**RESPONSE TO REQUIREMENT FOR RESTRICTION**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This responds to the Requirement for Restriction mailed May 31, 2006 in the above-identified application.

The Examiner has required restriction to one of the following inventions under 35 U.S.C. §121:

Group I. Claims 1-13 and 30-36, directed to a digital signal processor comprising a memory controller configured to enable the excess capacity of the write buffer and inhibit write access to the excess capacity of the write buffer based on a high priority task being serviced or not being serviced, classified in class 711, subclass 163.

Group II. Claims 14-29, directed to a digital signal processor comprising a memory controller configured to steer read data to the fill buffers based on priority of the fill operation, classified in class 711, subclass 158.

In response, Applicant hereby elects Group I, claims 1-13 and 30-36, for prosecution in the present application.

By this response, the application is now ready for examination on the merits. If there are questions regarding this response, please telephone Applicant's undersigned attorney.

Dated: June 28, 2006

Respectfully submitted,

By William R. McClellan  
William R. McClellan  
Registration No.: 29,409  
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